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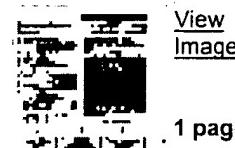View: INPADOC | Jump to: Top   Go to: Derwent...   ✉ Email this to a friend

Title: JP11007736A2: METHOD AND APPARATUS FOR DETECTING ERROR, DECODING APPARATUS, AND, RECORDING REPRODUCING APPARATUS
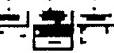
Country: JP Japan

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Abstract:

PROBLEM TO BE SOLVED: To improve error correction efficiency without increasing a byte length of an ECC when RLL(run length limit coded) recording data with the ECC(error-correcting code) added thereto are reproduced.
SOLUTION: A reproduction signal is subjected to input signal process (P600) and ECC run length adjustment (P506). Moreover, the signal is subjected to RLL error detection (P505). As a result, it is judged whether the signal can be an RLL code. On the occasion of an ECC error correction (P507), a position of an error code detected at the RLL error detection is used as an erasure flag. Data after the ECC error correction are subjected to RLL decoding and further to CRC(cyclic redundancy code) error detection, whereby user data are generated.
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Family: None

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